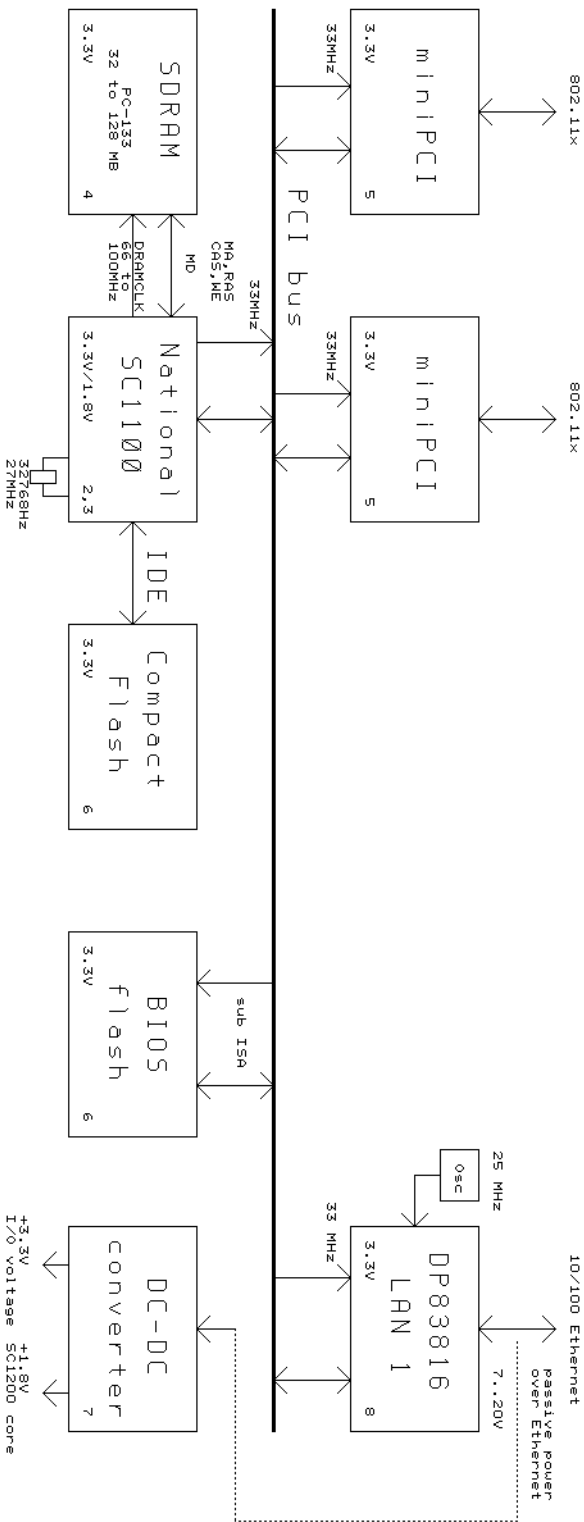


SC1100 wireless router platform

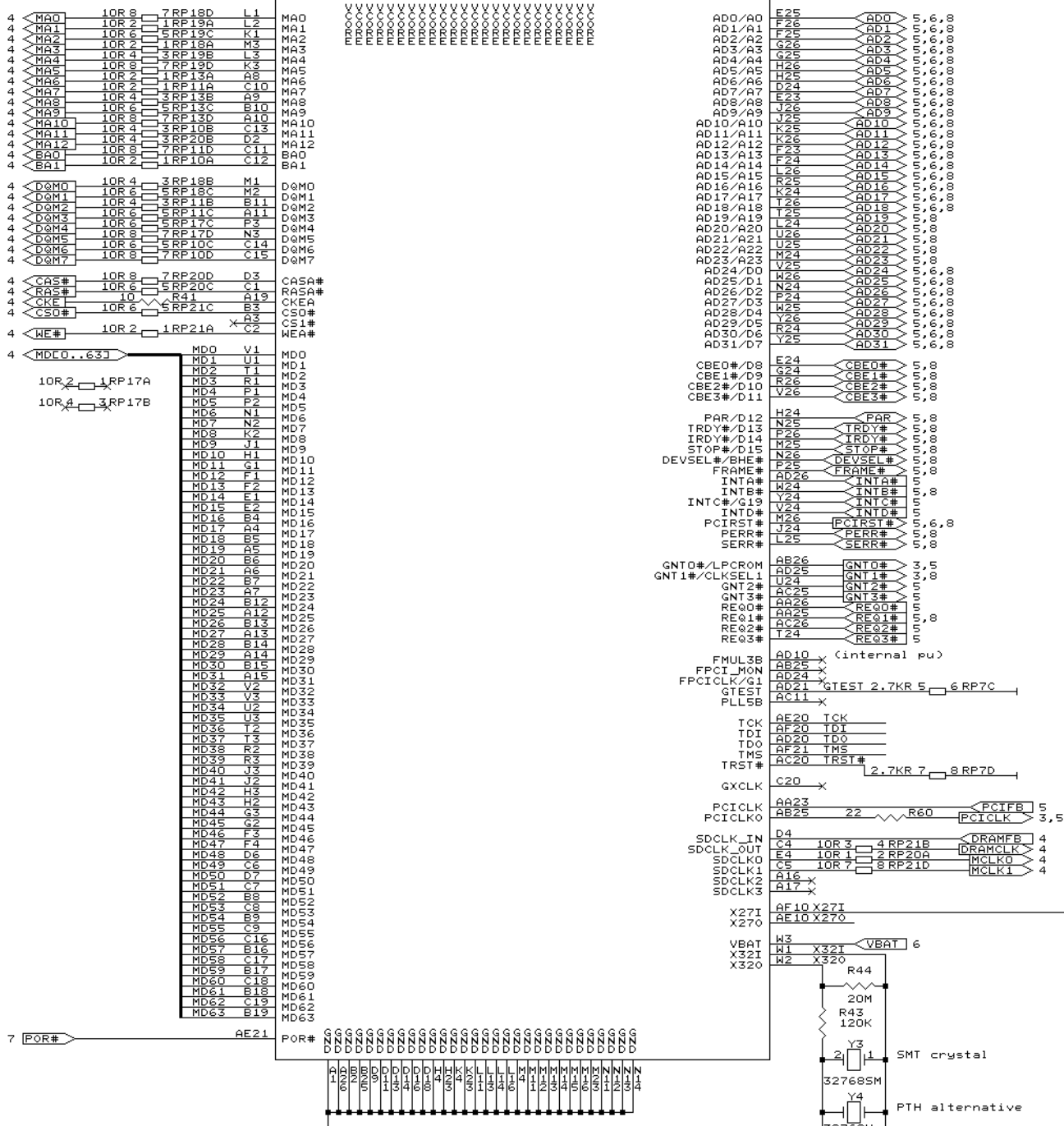


1: LINK
2: SCH
3: SCH
4: SCH
5: SCH
6: SCH
7: SCH
8: SCH

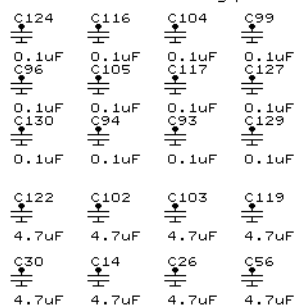
PC Engines GmbH			
CONFIDENTIAL			
Title Block Diagram			
Size Document Number		REV	
B WRAP		2E	
Date: November 29, 2003		Sheet 1 of 8	

Option A = 233 MHz CPU
Option B = 266 MHz CPU

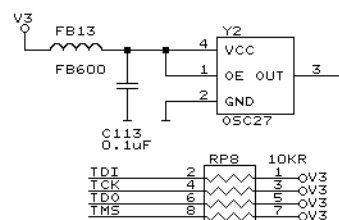
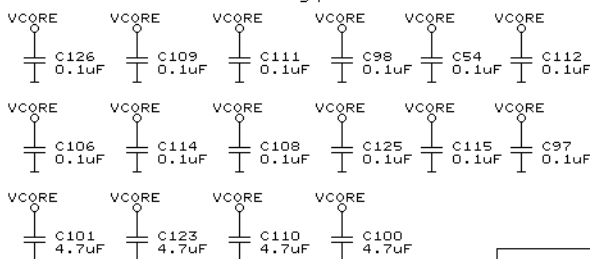
U10
SC1100_A



V3 (VIO) bypass



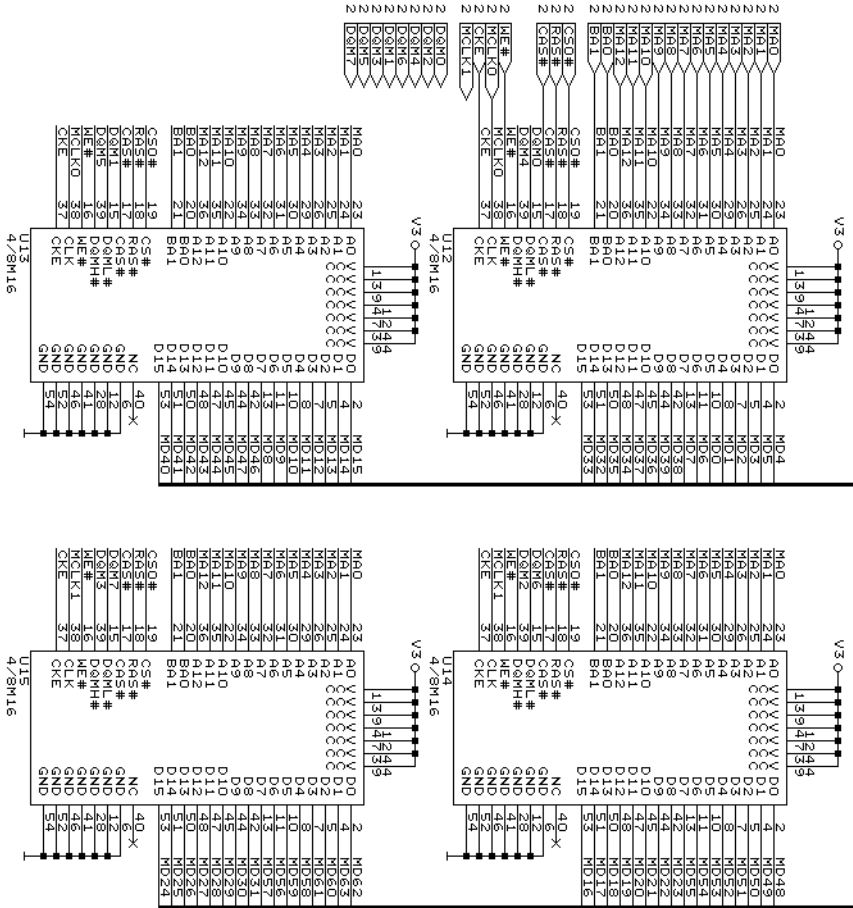
VCORE (1.8V) bypass



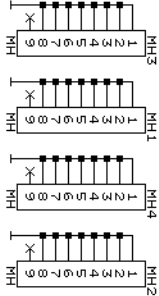
PC Engines GmbH			
CONFIDENTIAL			
Title			
National SC1100 CPU (1)			
Size	Document Number	REV	
A	WRAP	2E	
Date:	May 28, 2005	Sheet	2 of 8

Option C = 4Mx16 DRAM (32MB)
 Option D = 8Mx16 DRAM (64MB)
 Option E = 16Mx16 DRAM (128MB)

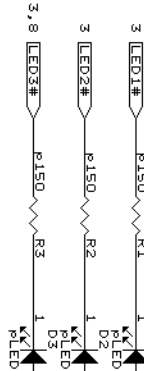
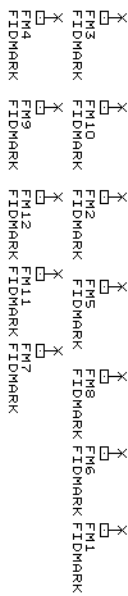
2 M0F0..633



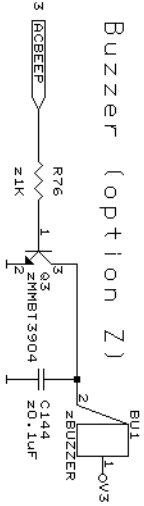
Mounting holes



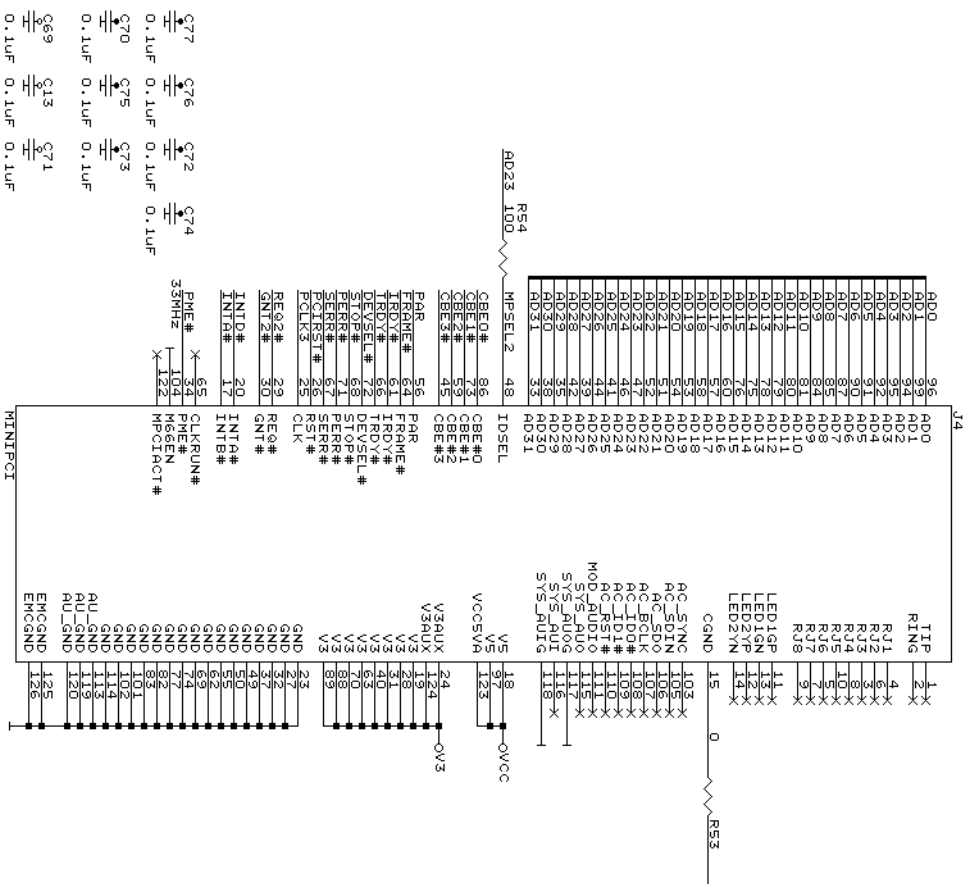
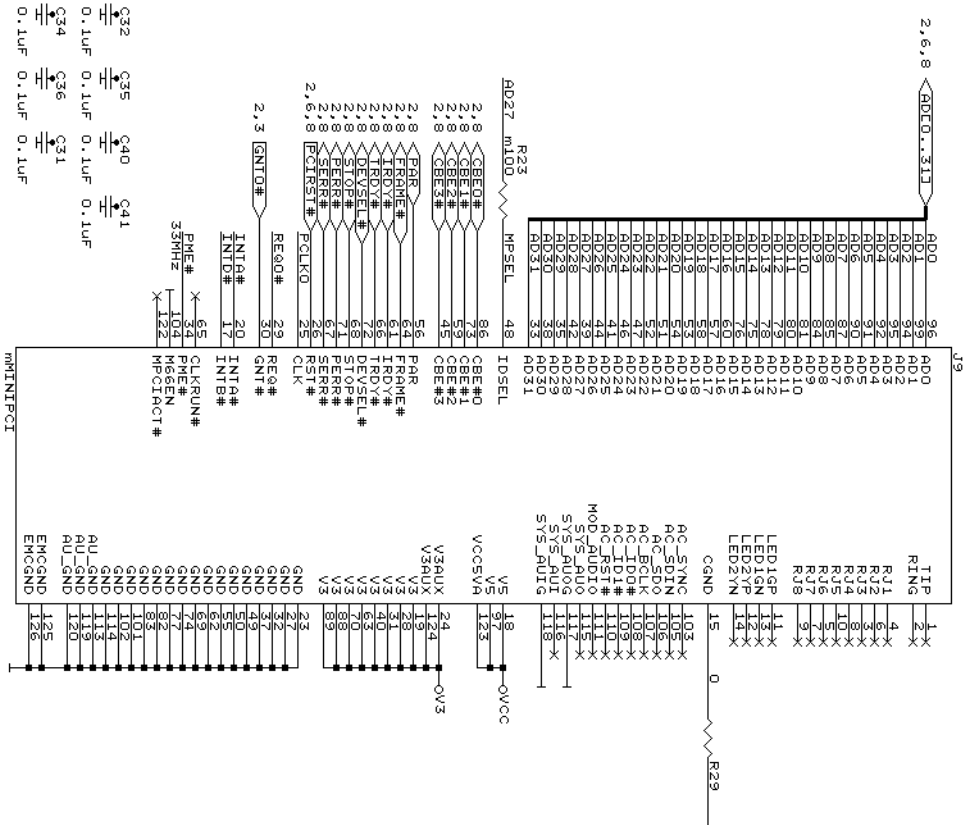
Fiducial marks



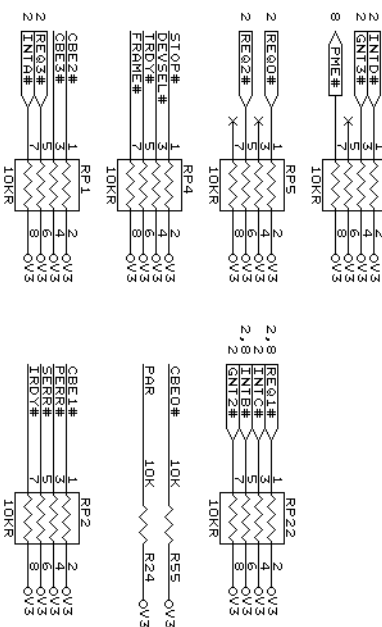
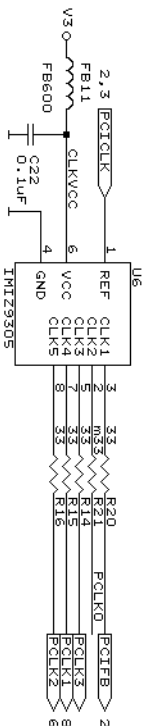
Buzzer (option Z)



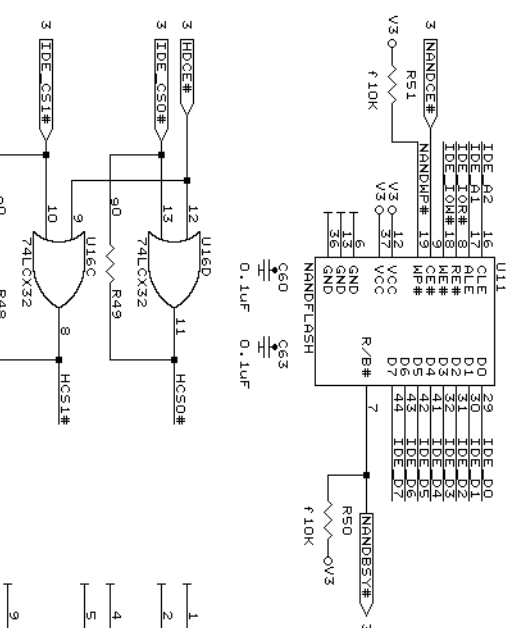
MiniPCI 2



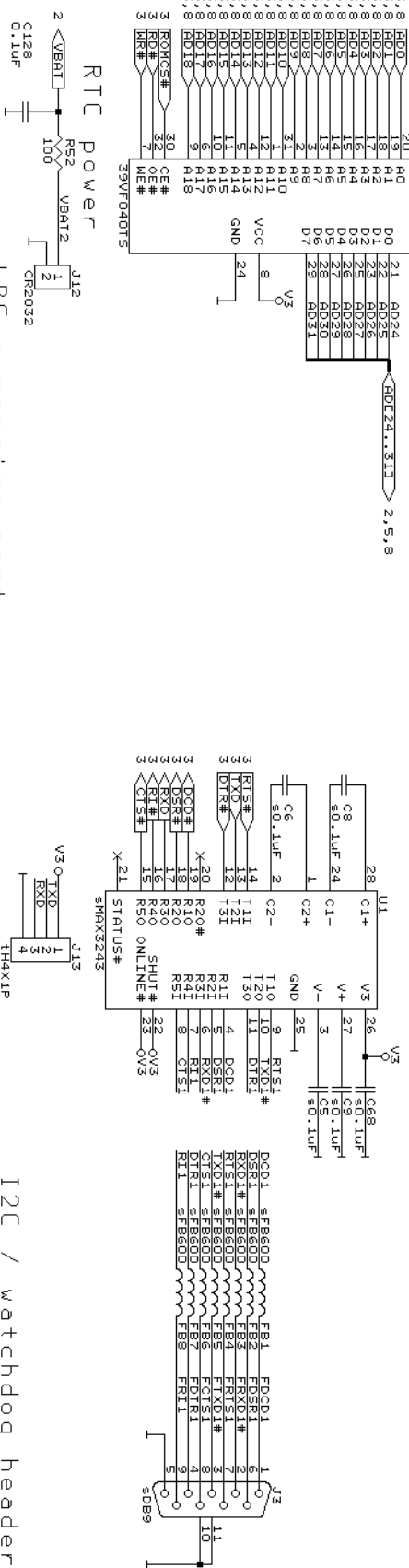
PCI clock buffer



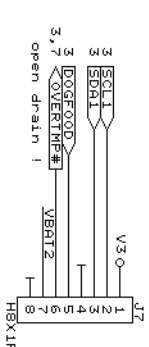
NAND flash



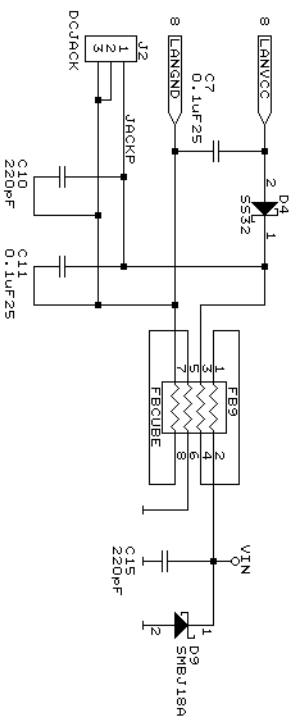
```
console serial port
```



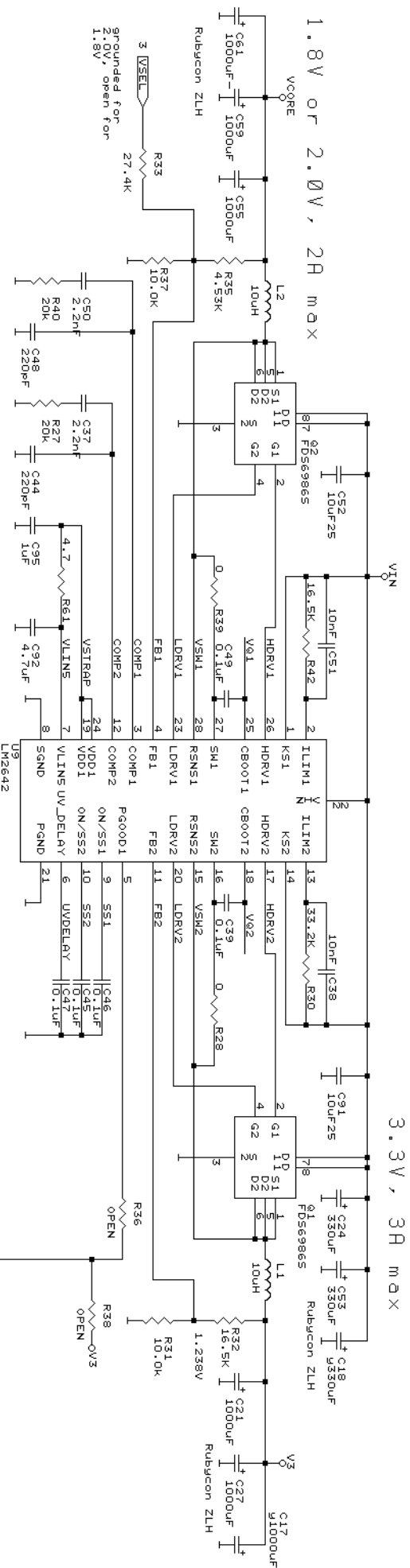
I2C / watchdog header



Power input +7..20V

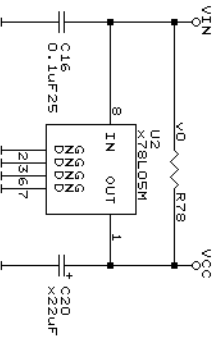


1.8V or 2.0V, 2A max

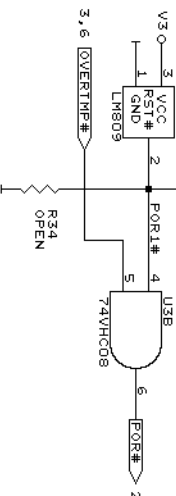


5V, 100mA max

Intended for bias, not supply !)

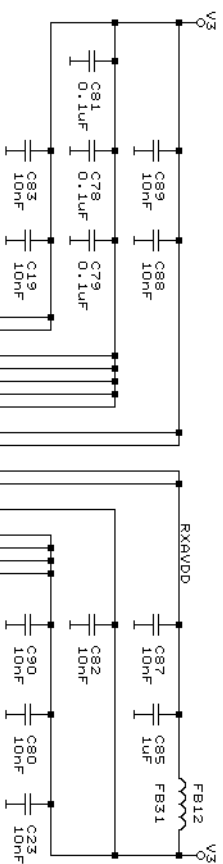


alternative to MBRO520L



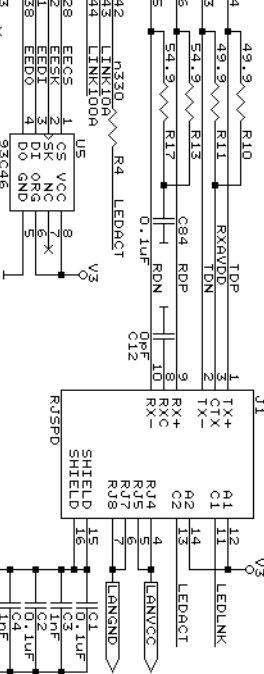
V3 3.3V discharge helps avoid reset problems
R7 ISO_1206

DP83816 10/100 Ethernet



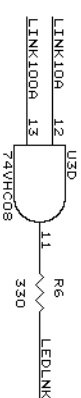
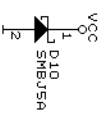
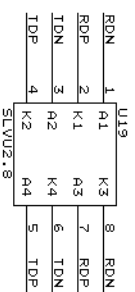
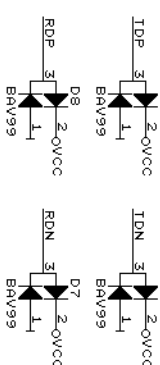
Alternative software LED
3.4 LED# > Q330 > R79 LEDACT

magnetics + RJ45



EEPROM

ESD diodes



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